

Inventor: Li Li et al.
Title: Processing Method of Forming MRAM Circuitry
Assignee: Micron Technology, Inc.

INFORMATION DISCLOSURE STATEMENT

PURSUANT TO 37 C.F.R. §§ 1.56, 1.97 AND 1.98

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, your attention is directed to the United States patents and other references listed on the attached Form PTO-1449. No admission is made regarding whether all the submitted references are prior art.

The listed references were cited by, or submitted to, the Office in the parent, co-pending application of the above-identified application. The above-identified application is a divisional application of co-pending application Serial No. 10/051,678, filed January 16, 2002. Such prior disclosure is sufficient for the above-identified application as far as copies of the references are concerned. 37 C.F.R. § 1.98(d) and MPEP § 609(2).

Citation of these references is respectfully requested.

Respectfully submitted,

Dated: 11-21-03

By: 

Mark S. Matkin
Reg. No. 32,268

Form PTO-1449

U.S. DEPARTMENT OF COMMERCE
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LIST OF ART CITED BY APPLICANT
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2824

U.S. PATENT DOCUMENTS							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA	6,599,814	7/2003	VANHAEMEERSCH ET AL.	438	431	
	AB	6,569,257	5/2003	NGUYEN ET AL.	134	26	
	AC	6,387,551	5/2002	NISHIOKA ET AL.	428	698	
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	AE	6,273,950	8/2001	KITABATAKE	117	95	
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	AG	5,965,463	10/1999	CUI ET AL.	438	723	
	AH	5,466,617	11/1995	SHANNON	438	155	
	AI	5,335,256	8/1994	MARUYAMA ET AL.	378	35	
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	AN	4,861,533	8/1999	BERTIN ET AL.	264	81	

FOREIGN PATENT DOCUMENTS								
		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No
	AO							
	AP							
	AQ							

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)

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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.